

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Eiji Nishibe et al.                      Art Unit : 2812  
Serial No. : 10/809,011                              Examiner : Walter Lee Lindsay, Jr.  
Filed : March 25, 2004                              Conf. No. : 7829  
Title : MANUFACTURING METHOD FOR A SEMICONDUCTOR DEVICE WITH  
REDUCED LOCAL CURRENT

**MAIL STOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF SEPTEMBER 20, 2006

Please amend the above-identified application as follows: